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August 20, 1999

**VENABLE**  
ATTORNEYS AT LAW

Attorney Docket: 32014-150502  
OKI

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Submitted herewith is the following patent application:

Title: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF  
MANUFACTURING THE SAME

Inventor(s):

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The priority of the following foreign application is claimed under 35 USC 119:  
Japan No. 234333/1998 filed August 20, 1998

The application includes

- ☒ a description pursuant to 37 C.F.R. 1.71;
- ☒ at least one claim pursuant to 37 C.F.R. 1.75;
- ☒ a drawing pursuant to 37 C.F.R. 1.81(a) [2 sheets]  
(Figs. 1-2).
- ☒ certified copy of Japan 234333/1998 filed August 20, 1998.

08/20/99  
JC639 U.S. PTO

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JC518 U.S. PTO  
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